



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: KURIHARA, Hideo et al.

Group Art Unit: 2826

Serial No.: 10/085,023

Examiner: Scott R. Wilson

Filed: March 1, 2002

P.T.O. Confirmation No.: 8810

For. TWO-BIT SEMICONDUCTOR MEMORY WITH ENHANCED CARRIER TRAPPING (AS AMENDED)

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: September 23, 2003

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated May 23, 2003 for one month from August 23, 2003 to September 23, 2003.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

39/26/2003 YPMLITEL U0000035 100**8**5023

Attorney for Applicant Reg. No. 44,450

TEB/kal Atty. Docket No. **020137** Suite 1000 1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

Customer Number $23850 \\ \text{PATENT TRADEMARK OFFICE}$